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[PDF] [► An Integrated Shared-Memory/Message Passing API for Cluster-Based Multicomputing](#)

E Speight, H Abdel-Shafi, JK Bennett - Proceedings of the Second IASTED International Conference on ..., 1998 - www-eece.rice.edu

... for a shared memory environment in the near term ... The data transferred in the message will be kept coherent ... obtained on a cluster of 4 four-processor Compaq 8000 ...

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[Method and apparatus for operating a cache memory in a multi-processor](#)

WD Hillis - US Patent 5,113,510, 1992 - [freepatentsonline.com](#)

... in the near future are likely to be near the current ... a cache memory in a multi-processor in which a ... in "Method and Apparatus for Routing MESSAGE Packets", Ser. ...

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[PS] [► Massively parallel methods for engineering and science problems](#)

WJ Camp, SJ Plimpton, BA Hendrickson, RW Leland - Communications of the ACM, 1994 - cs.sandia.gov

... which are capable of operating in an explicit message(passing MIMD ... to high performance on these machines is to achieve near(peak single(processor op rates ...

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[Scientific processor to support a host processor referencing common memory](#)

US Patent 4,873,630, 1989 - [freepatentsonline.com](#)

Title: Scientific processor to support a host processor referencing common memory. Document Type and Number: United States Patent ...

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[... with a serial multiport memory and a pseudo crossbar of serial links used sa processor-memeory](#)

...
D Litaize, O Hammami, M Lalam, A Mzoughi, P Sinrat - ACM SIGARCH Computer Architecture News, 1989 - portal.acm.org

... messages, a serial link is well suited to transmit them. ... The set of these curves shows that the current processor power and those forecast in a near ...

[Cited by 3](#) - [Related articles](#) - [Web Search](#) - [All 3 versions](#)

[CITATION] [Micros Join Intelligent Networks](#)

HA RAPHAEL - Microprocessors: New Directions for Designers, Selected from ..., 1975 - Hayden Book Co Inc., US

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[MetaH Support for Real-Time Multi-Processor Avionics](#)

S Vestal, HT Center - contract - [doi.ieeeecs.org](#)

... often provided by a common memory bus, or by connecting processors pair- wise in some topology using dual-port memories All inter-processor message passing is ...

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[A Hardware-Software Real-Time Operating System Framework for SoCs](#) - [gatech.edu](#) [pdf]

VJ Mooney, DM Blough - IEEE DESIGN & TEST, 2002 - [doi.ieeeecs.org](#)

... likely to become quite common in the near future ... using the shared-address-space or message-passing programming ... is required for compiler and processor design in ...

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A radar simulation program for a 1024-processor hypercube

JL Gustafson, RE Benner, MP Sears, TD Sullivan - Proceedings of the 1989 ACM/IEEE conference on ..., 1989 - portal.acm.org

... this approach creates severe load imbalance near the light ... Secondly, it doubles the grain size per processor. ... an error number with a special message type to ...

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A MASSIVELY-PARALLEL NAVIER-STOKES IMPLEMENTATION

WU Eng-Shien - pdf.aiaa.org

... studied as a function of processor characteristics other ... to predict performance for other message-passing commercial multiprocessors expected in the near-term. ...

[Web Search](#)

[CITATION] Report On Common Modular Architecture (CMA) For The Embedded GPS Receiver Robert C. Eckert Naval ...

S Center, C Memory - IEEE 1994 Position Location and Navigation Symposium: Las ..., 1994 - Order from IEEE Order Dept.

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Multiprocessors and Computer Networks

I Solomon, M Wisconsin - portal.acm.org

... sec rates may be available in the near future ... In effect, the communications processor is climbing up the ... as marshaling the contents of a message, determin- ing ...

[Web Search](#) - [All 3 versions](#)

[CITATION] by F. Gregoretti Department of Electronic Engineering Politecnico di Torino. Torino, Italy AM PROTOTYPE - Proceedings, 1982 - Institute of Electrical and Electronics Engineers

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... , PA, SIAM, 1987. M.-DA Huang, "Solving some graph problems with optimal or near-optimal speedup on ...

SL Tanimoto, TJ Ligocki, R Lmg... - IEEE TRANSACTIONS ON COMPUTERS, 1988 - doi.ieeecs.org

... graph problems with optimal or near- optimal speedup ... relevant for algorithms on a sequential processor. ... asynchronous processes shares a common memory space and ...

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A large scale, homogeneous, fully distributed parallel machine, I

H Sullivan, TR Bashkow, D Klappholz - ACM SIGARCH Computer Architecture News, 1977 - doi.ieeecomputersociety.org

... broadcast algorithm. Any processor can send a message to al_ other processors in the machine in just log ZN steps. This is accomplished ...

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3D graphics rendering system for performing Z value clamping in near-Z range to maximize scene ...

MM Leather - US Patent 6,618,048, 2003 - freepatentsonline.com

... 5163126, Method for adaptively providing near Phong grade ... 6008820, Processor for controlling the display of ... graphics subsystem with message-passing architecture, ...

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[PDF] ► Fault Tolerance in Tandem Computer Systems

J Bartlett, J Gray, B Horst - The Evolution of Fault-Tolerant Computing, 1987 - hpl.hp.com

... passing messages over the Dynabus. ... 6 Page 15. the bus near saturation. ... error detection is pipelined and does not stop the processor until ...

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System apparatus and method for storage device controller-based message passing having effective

...

NS Otterness, JG Skazinski - US Patent App. 09/836,806, 2001 - Google Patents

... primary processor and first auxiliary processor; and inter ... system than for communicating messages between controllers. ... to meet present or near-term requirements ...

[Cited by 2](#) - [Related articles](#) - [Web Search](#) - [All 4 versions](#)

Interprocessor communication in systems with distributed control

N Jovic, GW Couturier - Proceedings of the IEEE, 1977 - [ieeexplore.ieee.org](#)

... of three parts, the Near End (NE-near the processor ... This PC may be a dedicated processor for this function ... the message which is always preceded by a label and ...

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Ray Tracing: Parallelization via Image Decomposition and Performance Impact

BR Sklar, AK Somani - Proceedings of the, 1996 - [doi.ieeecomputersociety.org](#)

... because 8n grains produces optimal or near optimal speed ... of pipelining as well as image data message size were ... image results back to the master processor in a ...

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Key authors: [W Camp](#) - [J Gustafson](#) - [H Sullivan](#) - [J Bartlett](#) - [A Lai](#)

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